BE Rafael Muñoz Huerta

Rafael Muñoz Huerta obtained the Engineer's degree in Electronics with speciality in Digital Systems in Mazatlán's Technological Institute in 2006. Nowadays he(she) studies the Mastery in Electrical Engineering applied to the Processing sign in Hardware, in the Faculty(Power) of Mechanical Electrical and Electronic Engineering–Guanajuato University. His areas of interest include the Processing sign in Hardware using FPGA, and the accomplishment in FPGA of the technical Lock-In Digital for filtered not linearly.