

# Papers

## Books

- R. de J. Romero-Troncoso, *Electrónica Digital y Lógica Programable*, Ed. Universidad de Guanajuato, México 2007.

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- R. de J. Romero-Troncoso, *Sistemas Digitales con VHDL*, Ed. Legaria, México 2004.

## Journal Papers

- Osornio-Rios R. A., Romero-Troncoso R. de J., Herrera-Ruiz G., Castañeda-Miranda R., FPGA implementation of higher degree polynomial acceleration profiles for peak jerk reduction in servomotors. *Robotics and Computer-Integrated Manufacturing*, Elsevier, Vol. 25, 2009, pp 379-392.

- Franco-Gasca L. A., Romero-Troncoso R. de A., Herrera-Ruiz G., Peniche-Vera R. del R., FPGA based failure monitoring system for machining processes. *Int J Adv Manuf Technol*, Springer, Vol. 40, 2009, pp 676-686.

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