

MS José de Jesús Rangel Magdaleno

Jose de Jesus Rangel-Magdaleno received the BE laude degree on electronics engineering in 2006 from FIMEE-University of Guanajuato, Mexico, where he is currently working towards the MS degree in electrical engineering on hardware signal processing applications. Currently, he is an Assistant Professor within the Department of Electronics Engineering at FIMEE. His research interests include hardware signal processing and condition monitoring of electrical machines using FPGA.