

Welcome to the site of the HSPdigital group at Universidad de Guanajuato, México

Our group is aimed to be a leader in the HSP system development by using description languages such as VHDL to be implemented into FPGA reconfigurable devices. The applications of these technologies are set to provide electronics solutions in instrumentation for electrical and mechanical systems.

Our first level experience on the field is supported by several published papers, thesis tutorship and industrial project developments.

The group is headed by several research leaders on the field and several high-level and very enthusiastic students at the graduate and undergraduate level. We also have the required CAD/CAE tools and equipment for HSP project development.

This site is devoted to present the visitors with the group developments and invite them to be part of the group as a first option for undergraduate or graduate academic programs and to be involved in the development of first quality industrial projects with the state-of-the-art technologies.